



Sheet 1 of 1

Form 1449*	Atty. Docket No.: 884.448US1	Serial No. 09/967,240
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Sanu K. Mathew et al.	
	Filing Date: September 28, 2001	Group: 2121 8124

## U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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## FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
CD	Woo, R., et al., "A 670ps, 64bit Dynamic Low-Power Adder Design", <u>IEEE International Symposium on Circuits and Systems</u> , Geneva, Switzerland, pp. I-28-I31, (May 28-31, 2000)

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Examiner	Date Considered 8/16/04
*Substitute Disclosure Statement Form (PTO-1449)	

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2121



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sanu K. Mathew et al.  
Title: HIGH-PERFORMANCE ADDER

Docket No.: 884.448US1  
Filed: September 28, 2001  
Examiner:

Serial No.: 09/967,240  
Due Date: N/A  
Group Art Unit: 2121

Commissioner for Patents  
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- ☒ A return postcard.  
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By: Ann M. McCrackin  
Atty: Ann M. McCrackin  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 13 day of March, 2002.

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